Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	348	(semiconductor with (ic or integrated adj circuit)) and (passivat$4 with open$4) and resin and wiring and electrode	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/29 14:36
L2	82	(semiconductor with (ic or integrated adj circuit)) and (passivat$4 with open$4) and resin and wiring and (electrode with expos$3)	US-PGPUB	OR	ON	2005/12/29 14:38
L3	1	(semiconductor with (ic or integrated adj circuit)) and (passivat$4 with open$4) and (resin with (overlap adj second rim)) and wiring and (electrode with expos$3)	US-PGPUB	OR	ON	2005/12/29 14:38